20 Watt DC-DC Converters

SR Series

Single output of 5, 12, 15 or 24 V DC Input voltage up to 180 V DC

- Full input to output isolation 2.5 kV_{rms}
- · Wide input voltage range
- High efficiency up to 81%
- Efficient input filter
- · Very good dynamic characteristics
- · Parallel and series compatible
- · Continuous no-load and short circuit proof

Safety according to IEC/EN 60950



CE



Summary

The DC-DC converters of the SR series are power supply modules for electronic systems. Their major advantages include a high efficiency that remains virtually constant over the entire input voltage range, high reliability, low ripple and excellent dynamic response. The converters are designed for chassis mounting.

Type Survey and Key Data

Table 1: Type survey

Outpu	Output voltage Input voltage range and efficiency ²							Options			
U _{o nom} [V DC]	I _{o nom} [A]	U _{i min} U _{i max} 1945 V DC ¹	$\eta_{ m min}$ [%]	$\eta_{ ext{typ}}$ [%]	U _{i min} U _{i max} 3890 V DC ¹	η_{\min} [%]	$\eta_{ ext{typ}}$ [%]	U _{i min} U _{i max} 76180 V DC	$\eta_{ m min}$ [%]	$\eta_{ ext{typ}}$ [%]	
5	4	BSR 2005-7	70	73	CSR 2005-7	74	77	DSR 2005-7	74	77	-6, R ¹ , i, P
12	1.67	BSR 2012-7	74	77	CSR 2012-7	75	78	DSR 2012-7	75	78	
15	1.34	BSR 2015-7	74	78	CSR 2015-7	76	79	DSR 2015-7	76	79	
24	0.84	BSR 2024-7	75	79	CSR 2024-7	77	81	DSR 2024-7	77	81	

¹ With option R, shifted input voltage range, for BSR: 16...38 V DC, for CSR: 32...76 V DC. Not available for DSR.

² Efficiency at $U_{i nom}$ and $I_{o nom}$.

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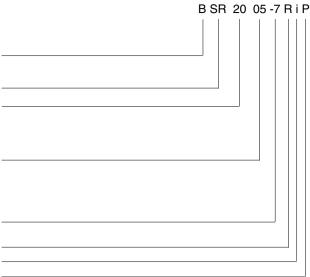




Type Key and Product Marking

Туре Кеу

Input voltage range <i>U</i> i		
1945 V DC	B	
3890 V DC		_
76180 V DC	D	
Series	. SR	_
Nominal output power in watt	20	_
Nominal output voltage in volt		
5 V	05	
12 V	12	_
15 V		
24 V	24	
Ambient temperature range $T_A = -2571 ^{\circ}\text{C}$:		
Plastic semi-conductors	7	_
Hermetic semi-conductors (option)	6	
Options: Shifted input voltage range	R	_
Inhibit input		_
Potentiometer	P	-



Example: BSR 2005-7RiP = 20 Watt DC-DC converter with a 5 V output, ambient temperature range of -25...71 °C, shifted input range of $U_i = 16...38$ V DC, inhibit, and potentiometer.

Produkt Marking

Basic type designation, applicable safety approval and recognition marks, CE mark, warnings and Melcher company logo. Specific type designation, input voltage range, nominal output voltage and current, pin allocation, identification of LED and potentiometer.

Label with batch No., serial No. and data code comprising production site, modification status of the main PCB and date of production.





Functional Description

After passing through a line interference filter, the DC voltage is chopped at a constant frequency of about 30 kHz. After transformer isolation and rectification, an output filter reduces ripple to a minimum without compromising the dynamic behaviour. The output voltage is routed to a control circuit with internal sense lines, where the output voltage is compared with a reference voltage. The voltage is regulated via a control transformer by varying the duty ratio of the forward converter.

The converters have input transient protection by means of a zinc oxide VDR and input reverse polarity protection by means of a protective diode and a fuse.

An internal inhibit signal keeps the output voltage switched off below approx. 0.7...0.9 $U_{i \text{ min}}$.

The outputs are protected against continuous open circuit by means of an internal, electronically switched pre-load and against continuous short circuit by the current limiting circuit. No output voltage overshoot will occur when plugging in or disconnecting the module, switching the input voltage on/ off, inhibiting the unit or after reset.

In the event of a main control circuit failure, the maximum output voltage $U_{\rm o}$ is restricted to approx. 1.2 $U_{\rm o nom}$ by means of an independent second control circuit.

When the converters are located in free, stationary air at a temperature $T_{A max} = 71 \,^{\circ}C$ and are operated at nominal power, the case temperature $T_{C max}$ will reach approx. 85 $\,^{\circ}C$ after a certain warm-up time (Case temperature T_{C} at the measuring point, see: *Mechanical Data*).

Under certain operating conditions the ambient temperature $T_{A \text{ max}}$ may be allowed to exceed 71 °C provided that additional measures are taken to ensure the case temperature $T_{C \text{ max}}$ remains below 85 °C.

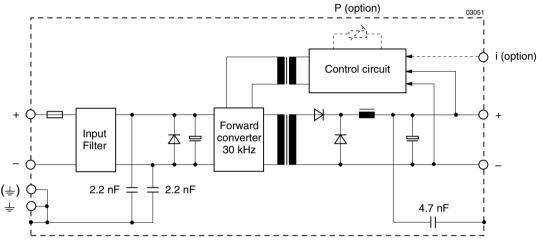


Fig. 1 Block diagram



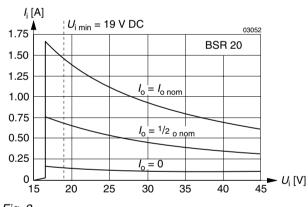


Electrical Input Data

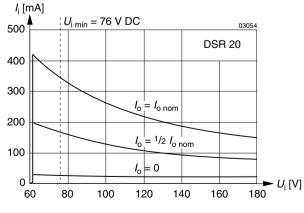
- General Conditions
- $-T_{\rm A} = 25^{\circ}{\rm C}$, unless $T_{\rm C}$ is specified
- With option i, pin i is connected with $-U_o$
- With option P, $U_{\rm o} = U_{\rm o nom}$ at $I_{\rm o nom}$

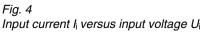
Table 2: Input data

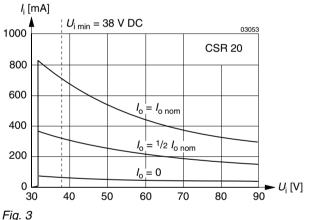
Input					BSR			CSR			DSR		
Charac	teristics		Conditions	min	typ	max	min	typ	max	min	typ	max	Unit
Ui	Input	Standard	$I_{\rm o}=0I_{\rm o nom}$	19	(30)	45	38	(60)	90	76	(120)	180	V DC
(U _{inom})	voltage	Option R	$T_{C \min} \dots T_{C \max}$	16	(24)	38	32	(48)	76		n.a.		
l _{inr p}	l _{inr p} Peak inrush current U _{i nom}		<i>U</i> i nom		28			22			10		Α
t _{inr r} Rise time				0.2			0.3			0.4		ms	
t _{inr h}					1			0.7			1		
<i>U</i> i RFI	RFI level a 0.0130 l		EN 55022 50 Ω/50 μH <i>U</i> _i = <i>U</i> _{i nom}			В			В			В	
Si	Primary fu	ise rating	slow blow		3.15			2			1.6		Α
<i>u</i> i tr			1.2/5 μs VDE 0433	0.8			1.5			2.5			kV
l _{i inh}		nput current erter inhibited	5 min. after start		16			14			14		mA
l _{i uv}	Input curre undervolta	ent age lock-out	after 5 min. $U_{\rm i} < 0.7 U_{\rm i min}$		1			1			1		





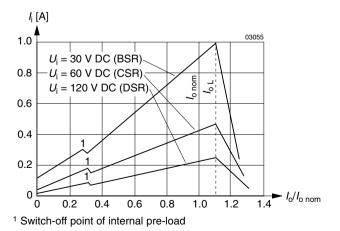


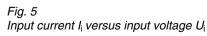






Input current Ii versus input voltage Ui







Electrical Output Data

General Conditions

 $- T_{\rm A} = 25 \,^{\circ}{\rm C}$, unless $T_{\rm C}$ is specified

– With option i, pin i is connected with – U_{o}

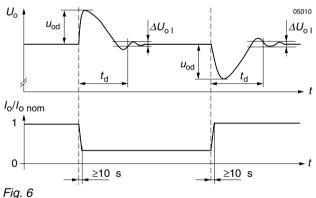
- With option P, $U_o = U_{o nom}$ at $I_{o nom}$

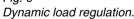
Table 3: Output data

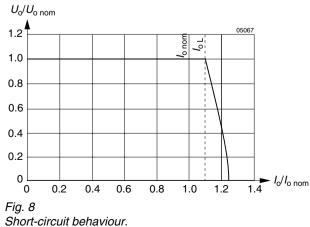
Outpu	Output		SR 2005		SR 2012		SR 2015		SR 2024							
Chara	Characteristics C		Conditions	min	typ	max	min	typ	max	min	typ	max	min	typ	max	Unit
Uo	Output vo	Itage	U _{i nom} , I _{o nom}	4.97		5.03	11.92		12.07	14.91		15.09	23.85		24.14	V DC
I _o	Output cu	rrent	U _{i min} U _{i max}	0		4	0		1.67	0		1.34	0		0.84	A
I _{o L}	Output cu	rrent limitation	U _{i min} U _{i max} T _{C min} T _{C max}		4.8			1.84			1.47			0.92		
U _{o L}	Overvoltag	ge limit with ntrol loop	Failure of main control circuit	5.3		6.8	13.5		16.0	16.5		19.0	27.0		31.0	V DC
uo		ut voltage / = 20 MHz) ¹	U _{i min} U _{i max} I _{o nom}		30	40		10	20		10	20		10	20	mV _{pp}
ΔU _{oU}	Static line	regulation			±12	±24		±10	±20		±10	±20		±12	±25	mV
ΔU _{ol}	Static load	regulation	$U_{i nom}$ $I_o = 0I_{o nom}$		12	25		20	40		25	50		30	60	
U _{od}	Dynamic	Voltage deviat.			±70			±25			±25			±25		
t _d	load re- gulation	Recovery time	$1 \leftrightarrow \frac{1}{5} I_{o nom}$ IEC/EN 61204		70			150			200			300		μs
α _{Uo}	· ·	ire coefficient	$U_{\rm i min} \dots U_{\rm i max}$			±1			±2			±3			±5	mV/K
	$\Delta U_{\rm o}/\Delta T_{\rm C}$		$I_{\rm o} = 0I_{\rm o nom}$			±0.02			±0.02			±0.02			±0.02	%/K

¹ According to IEC/EN 61204. See: Technical Information: Measuring and Testing.

² See: Dynamic Characteristics.







Output voltage U_{0} versus output current I_{0} .

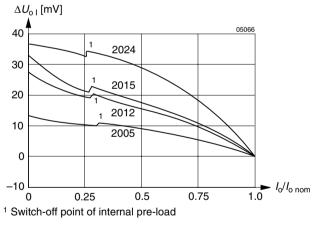


Fig. 7

Static load regulation. Output voltage U_0 versus output current I_0 .





SR Series

Thermal Considerations

If a converter is located in free, quasi-stationary air (convection cooling) at the indicated maximum ambient temperature $T_{A max}$ (see table: *Temperature specifications*) and is operated at its nominal input voltage and output current, the temperature measured at the *Measuring point of case temperature* T_{C} (see: *Mechanical Data*) will approach the indicated value $T_{C max}$ after the warm-up phase. However, the relationship between T_{A} and T_{C} depends heavily on the conditions of operation and integration into a system. The

thermal conditions are influenced by input voltage, output current, airflow and temperature of surrounding components and surfaces. $T_{A \text{ max}}$ is therefore, contrary to $T_{C \text{ max}}$, an indicative value only.

Caution: The installer must ensure that under all operating conditions $T_{\rm C}$ remains within the limits stated in the table: *Temperature specifications.*

Note: Sufficient forced cooling or an additional heat sink allows T_A to be higher than 71 °C (e.g. 85 °C) as long as $T_{C \text{ max}}$ is not exceeded.

Immunity to Environmental Conditions

Test Method		Standard	Test Conditions	Status	
Ca	Damp heat steady state	IEC/DIN IEC 60068-2-3 MIL-STD-810D section 507.2	Temperature: Relative humidity: Duration:	40 ^{±2} °C 93 + ^{2/-3} % 56 days	Unit not operating
Ea	Shock (half-sinusoidal)	IEC/EN/DIN EN 60068-2-27 MIL-STD-810D section 516.3	Acceleration amplitude: Bump duration: Number of bumps:	100 g _n = 981 m/s ² 6 ms 18 (3 each direction)	Unit operating
Eb	Bump (half-sinusoidal)	IEC/EN/DIN EN 60068-2-29 MIL-STD-810D section 516.3	Acceleration amplitude: Bump duration: Number of bumps:	40 g _n = 392 m/s ² 6 ms 6000 (1000 each direction)	Unit operating
Fc	Vibration (sinusoidal)	IEC/EN/DIN EN 60068-2-6 MIL-STD-810D section 514.3	Acceleration amplitude: Frequency (1 Oct/min): Test duration:	0.35 mm (1060 Hz) 5 g _n = 49 m/s ² (602000 Hz) 102000 Hz 7.5 h (2.5 h each axis)	Unit operating

Table 5: Temperature specifications, valid for air pressure of 800...1200 hPa (800...1200 mbar)

Tem	perature	Stand	lard -7 ³	Opt	Option -6 ⁴		
Char	acteristicsConditions		min	max	min	max	Unit
TA	Ambient temperature ¹	Operational ²	-25	71	-25	71	°C
T _C	Case temperature		-25	85	-25	85	
Ts	Storage temperature 1	Non operational	-40	85	-40	85	

¹ MIL-STD-810D section 501.2 and 502.2

² See: Thermal Considerations

³ Plastic semiconductor devices

⁴ Hermetic semiconductor devices

Table 6: MTBF and device hours

МТВБ	Ground Fixed		Ground	Mobile
MTBF acc. to MIL-HDBK-217D	$T_{\rm C} = 40^{\circ}{\rm C}$	$T_{\rm C} = 70^{\circ}{\rm C}$	$T_{\rm C} = 40^{\circ}{\rm C}$	<i>T</i> _C = 70°C
Standard -7	155'000 h	69'000 h	31'500 h	13'000 h
Option -6	100'000 h	46'000 h	28'000 h	18'000 h

¹Statistical values, based on an average of 4300 working hours per year and in general field use





SR Series

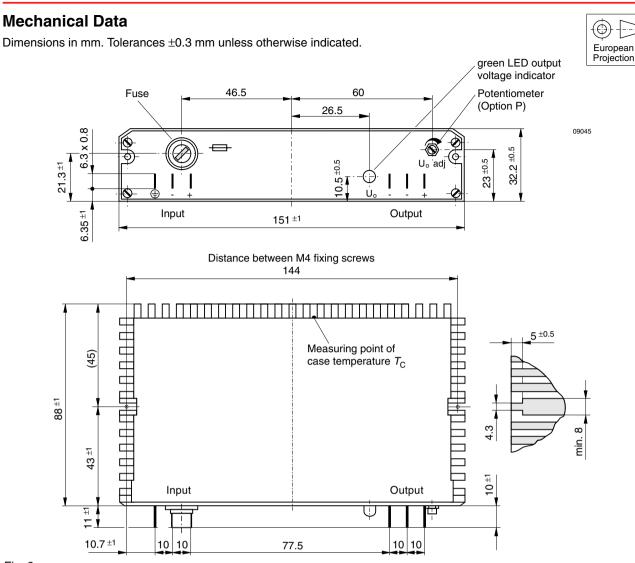


Fig. 9

Case: C01 (weight: BSR 550 g, CSR and DSR 555 g). Aluminium, black finish, fully enclosed, self cooling.





Safety and Installation Instructions

Installation Instructions

Our DC-DC converters are components, intended exclusively for inclusion within other equipment by an industrial assembly operation or by professional installers. Installation must strictly follow the national safety regulations in compliance with the enclosure, mounting, creepage, clearance, casualty, markings and segregation requirements of the end-use application. See also: *Technical Information: Installation and Application.*

Connection to the system shall be made via the 6.3×0.8 mm faston contacts at the cover side of the unit according to *Mechanical Data*.

The DC-DC converters are provided with an earth pin (\textcircled) , which is reliably connected with their case. For safety reasons it is essential to connect this pin with the protective earth of the supply system if required in: *Safety of operator accessible output circuit*.

An input fuse is built-in in the connection from the Vi+ terminal of the unit. Since this fuse is designed to protect the unit in case of an overcurrent and does not necessarily cover all customer needs, an external fuse suitable for the application and in compliance with the local requirements should be installed in the wiring to one or both input pins (Vi+ and/ or Vi–).

Important: Whenever the inhibit function is not in use, pin i should be connected to pin Vo1– to enable the output. Do not open the modules, or guarantee will be invalidated.

Make sure that there is sufficient air flow available for convection cooling. This should be verified by measuring the case temperature when the unit is installed and operated in the end-use application. The maximum specified case temperature $T_{\rm C\ max}$ shall not not be exceeded. See also: *Thermal Considerations*.

Cleaning Agents

In order to avoid possible damage, any penetration of cleaning fluids is to be prevented, since the power supplies are not hermetically sealed.

Standards and Approvals

The DC-DC converters correspond to class I equipment. All types are LGA approved to IEC/EN 60950 standards.

The units have been evaluated for:

- Building in,
- Basic insulation between input and case/output, based on 250 V AC and 400 V DC,
- Basic insulation between output and case, based on 250 V AC and 400 V DC,
- The use in a pollution degree 2 environment,
- Connecting the input to a circuit which is subject to a maximum transient rating of 2500 V.

The DC-DC converters are subject to manufacturing surveillance in accordance with EN 60950 and ISO 9001 standards.

Protection Degree

The protection degree of the DC-DC converters is IP 40, exept in the vicinity of the terminals, where it depends on the installation.

Isolation

The electric strength test is performed as factory test in accordance with IEC/EN 60950 and UL 1950 and should not be repeated in the field. Melcher will not honour any guarantee claims resulting from electric strength field tests.

Characte	Input to case	Input to output	Output to case	Unit	
Electric strength	Required according to IEC/EN 60950	2.0	2.5 ¹	1.0	kV _{rms}
test voltage	Actual factory test 1 s	2.8	3.5 ¹	1.4	kV DC
Insulation at 500 V	>100	>100	>100	MΩ	

¹ In accordance with IEC/EN 60950 only subassemblies are tested in factory with this voltage.





Safety of operator accessible output circuit

If the output circuit of a DC-DC converter is operator accessible, it shall be an SELV circuit according to the IEC/EN 60950 related safety standards

The following table shows some possible installation configurations, compliance with which causes the output circuit of the DC-DC converter to be an SELV circuit according to

Table 8: Insulation concept leading to an SELV output circuit

IEC/EN 60950 up to a configured output voltage (sum of nominal voltages if in series or +/– configuration) of 44 V.

However, it is the sole responsibility of the installer to assure the compliance with the relevant and applicable safety regulations. More information is given in: *Technical Information: Safety*.

Conditions	Front end			DC-DC converter	Result
Supply voltage	Minimum required grade of isolation, to be provided by the AC-DC front end, including mains supplied battery charger	Maximum DC output voltage from the front end ¹	Minimum required safety status of the front end output circuit	Measures to achieve the specified safety status of the output circuit	Safety status of the DC-DC converter output circuit
Mains ≤250 V AC	Operational (i.e. there is no need for electrical isolation between the main supply voltage and the DC-DC converter input voltage)	≤180 V ¹	Primary circuit	Basic insulation, based on 250 V AC (provided by the DC-DC converter), input fuses ² , output suppressor diode ³ , earthed output circuit and earthed ⁴ or non user accessible case ⁵	Earthed SELV circuit
	Basic	≤60 V ⁶	Earthed SELV circuit ⁴	Operational insulation (pro- vided by the DC-DC converter)	SELV circuit
	Supplementary		ELV circuit	Basic insulation, based on	
		>60 V ¹	Unearthed hazardous voltage secondary circuit ⁵	250 V AC (provided by the DC-DC converter)	
	Double or reinforced	≤60 V ¹	SELV circuit	Operational insulation (pro- vided by the DC-DC converter)	
		\leq 120 V 6	TNV-3 circuit	Basic insulation, based on the	
		>60 V ¹	Double or reinforced insu- lated unearthed hazardous voltage secondary circuit ⁶	maximum output voltage from the front end (provided by the DC-DC converter)	

¹ The front end output voltage should match the specified input voltage range of the DC-DC converter. The nominal voltage between any input pin and earth can be up to 250 V AC or 400 V DC.

² The installer shall provide an approved fuse (type with the lowest rating suitable for the application) in both input lines directly at the input of the DC-DC converter (see fig.: *Schematic safety concept*). For UL's purpose, the fuse needs to be UL-listed.

³ The suppressor diode should be dimensioned in such a way, that in the case of an insulation fault the diode is able to limit the output voltage to SELV (<60 V) until the input fuse blows (see fig.: *Schematic safety concept*).

- ⁴ The earth connection has to be provided by the installer according to the relevant safety standard, e.g. IEC/EN 60950 (see also fig.: *Schematic safety concept*).
- ⁵ Has to be insulated from earth by at least supplementary insulation according to the relevant safety standard, based on the maximum output voltage from the front end.

⁶ The front end output voltage should match the specified input voltage range of the DC-DC converter.

⁷ Has to be insulated from earth by at least supplementary insulation according to the relevant safety standard, based on the maximum mains supply voltage.

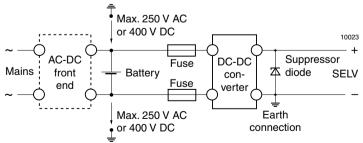


Fig. 10

Schematic safety concept. Use fuse, suppressor diode and earth connection as per table Safety concept leading to an SELV output circuit.





Description of Options

-6 Hermetic semiconductor

The converters are equipped with hermetic semiconductors instead of the standard plastic ones as far as such components are electrically and mechanically compatible.

P Potentiometer

The potentiometer provides an output voltage adjustment range of $\pm 5\%$ from the nominal output voltage $U_{o nom}$ and is accessible from the connector side. This option can be used, for example, to compensate for voltage drops in the supply lines.

R Shifted input voltage range

Option R allows the operation of BSR and CSR converter series from either 24 or 48 V input voltages. The input voltage tolerance is -30...25% as required by IEC 571: *Rules for Electronic Equipment used on Rail Vehicles*.

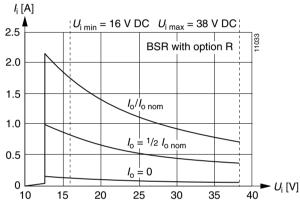
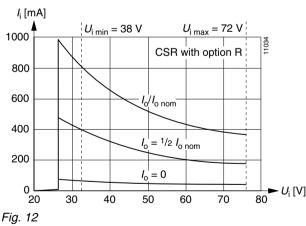
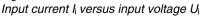


Fig. 11 Input current l_i versus input voltage U_i

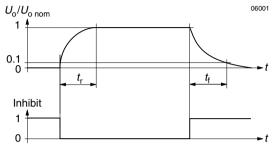




i Inhibit

The DC-DC converter can be switched on or off with a control signal via the inhibit input i. In systems with several units, this feature can be used, for example, to control the activation sequence of the units with a logic signal (TTL, C-MOS, etc.).

Note: A non-used inhibit input should be connected to the negative output to enable the unit.





Output response as a function of inhibit signal

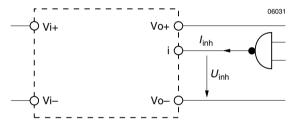


Fig. 14 Definitions of I_{inh} and U_{inh}

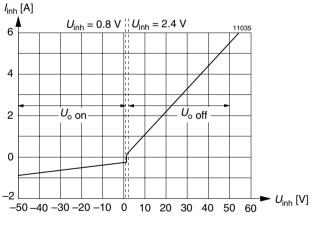


Fig. 15 Inhibit current l_{inh} versus inhibit voltage U_{inh}





Table 9: Inhibit characteristics

Characteristics			Conditions	SR 2005			SR 2012			SR 2015			SR 2024			Unit
				min	typ	max										
U _{inh}	Inhibit input voltage to	$U_{\rm o} = {\rm on}$	U _{i min} U _{i max} T _{C min} T _{C max}	-50		+0.8	-50		+0.8	-50		+0.8	-50		+0.8	V DC
	keep output voltage	$U_{\rm o} = {\rm off}$	$R_{\rm L} = U_{\rm o nom}/I_{\rm o nom}$	+2.4		+50	+2.4		+50	+2.4		+50	+2.4		+50	
l _{inh}	Inhibit input current at $U_{inh} = 0$					-400			-400			-400			-400	μA
<i>t</i> r	Switch-on time after inhibit command		$R_{\rm L} = U_{\rm o nom}/I_{\rm o nom}$ $U_{\rm i nom}$	40 2		80			80			110		ms		
t _f	Switch-off time after inhibit command					75			75			80				



